

U.S. Serial No. 10/608,327

Response to the Office action of April 26, 2006

This listing of claims will replace all prior versions, and listings, of claims in the application:

The Status of the Claims

1. Cancelled

2. Cancelled

3. Cancelled

4. Cancelled

5. Cancelled

6. Cancelled

7. Cancelled

8. (Withdrawn) The external cavity optical transmitter as defined in claim 5, further comprising:

a variable optical phase retardation device disposed between the gain chip and the reflector, wherein the variable optical phase retardation device retards a phase of optical energy passing between the gain chip and the reflector by varying a refractive index of the variable optical phase retardation device; and

a voltage source coupled to the variable optical phase retardation device and controlled to maintain the refractive index of the variable optical phase retardation device at a level at which the operating current of the gain chip is minimized.

9. (Withdrawn) The external cavity optical transmitter as defined in claim 8, wherein the voltage source is controlled to vary the refractive index of the variable optical phase retardation device to determine a phase retardation at which the operating current of the gain chip is minimized.

10. Cancelled

11. (Original) A method of operating an external cavity optical transmitter including a gain chip and an optical modulator coupled to a temperature controlled substrate having an associated temperature, the method comprising:

U.S. Serial No. 10/608,327

Response to the Office action of April 26, 2006

maintaining the temperature controlled substrate, the gain chip and the optical modulator between a first temperature and a second temperature between which the optical modulator has an acceptable performance characteristic;

varying the temperature of the temperature controlled substrate, the gain chip and the optical modulator from the first temperature to the second temperature;

measuring an operating current supplied to the gain chip as the temperature of the temperature controlled substrate, the gain chip and the optical modulator varies from the first temperature to the second temperature;

determining an optimum temperature between the first temperature and the second temperature that corresponds to a minimum operating current; and

maintaining the temperature of the temperature controlled substrate, the gain chip and the optical modulator at the optimum temperature.

12. (Original) The method as defined in claim 11, wherein a temperature difference between the first temperature and the second temperature comprises about two degrees Centigrade.

13. (Original) The method as defined in claim 11, wherein determining an optimum temperature comprises determining operating current as a function of temperature.

14. (Original) The method as defined in claim 13, wherein determining an optimum temperature comprises taking a derivative of the operating current as a function of temperature.

15. (Original) The method as defined in claim 13, wherein determining an optimum temperature comprises taking first and second derivatives of the operating current as a function of temperature.

16. (Original) The method as defined in claim 11, further comprising:

measuring the operating current and comparing it to the minimum operating current; and

if a difference between the operating current and the minimum operating current exceeds a predetermined threshold, (a) varying the temperature of the temperature controlled substrate, the gain chip and the optical modulator from the first temperature to the second

U.S. Serial No. 10/608,327

Response to the Office action of April 26, 2006

temperature, (b) measuring the operating current supplied to the gain chip as the temperature of the temperature controlled substrate, the gain chip and the optical modulator varies from the first temperature to the second temperature, (c) determining a second optimum temperature between the first temperature and the second temperature that corresponds to a second minimum operating current, and (d) maintaining the temperature of the temperature controlled substrate, the gain chip and the optical modulator at the second optimum temperature.

17. (Withdrawn) A method of operating an external cavity optical transmitter including a gain chip, an optical modulator and an electro-optic crystal coupled to a temperature controlled substrate having an associated temperature, the method comprising:

maintaining the temperature controlled substrate, the gain chip and the optical modulator between a first temperature and a second temperature between which the optical modulator has a predefined performance characteristic;

varying a bias voltage applied to the electro-optic crystal from a first voltage to a second voltage;

measuring an operating current supplied to the gain chip as the bias voltage applied to the electro-optic crystal varies from the first voltage to the second voltage;

determining an optimum bias voltage between the first voltage and the second voltage that corresponds to a minimum operating current; and

maintaining the bias voltage applied to the electro-optic crystal at the optimum bias voltage.

18. (Withdrawn) The method as defined in claim 17, wherein determining an optimum bias voltage comprises determining operating current as a function of bias voltage.

19. (Withdrawn) The method as defined in claim 18, wherein determining the optimum bias voltage comprises taking a derivative of the operating current as a function of bias voltage.

20. (Withdrawn) The method as defined in claim 18, wherein determining the optimum bias voltage comprises taking first and second derivatives of the operating current as a function of bias voltage.

U.S. Serial No. 10/608,327

Response to the Office action of April 26, 2006

21. (Withdrawn) The method as defined in claim 17, further comprising:

measuring the operating current and comparing it to the minimum operating current;

and

if a difference between the operating current and the minimum operating current becomes significant, (a) varying the bias voltage applied to the electro-optic crystal from the first voltage to a second voltage, (b) measuring the operating current supplied to the gain chip as the bias voltage applied to the electro-optic crystal varies from the first voltage to the second voltage, (c) determining a second optimum bias voltage that corresponds to a second minimum operating current, and (d) maintaining the bias voltage applied to the electro-optic crystal at the second optimum bias voltage.

22. Cancelled

23. Cancelled

24. Cancelled

25. Cancelled

26. Cancelled